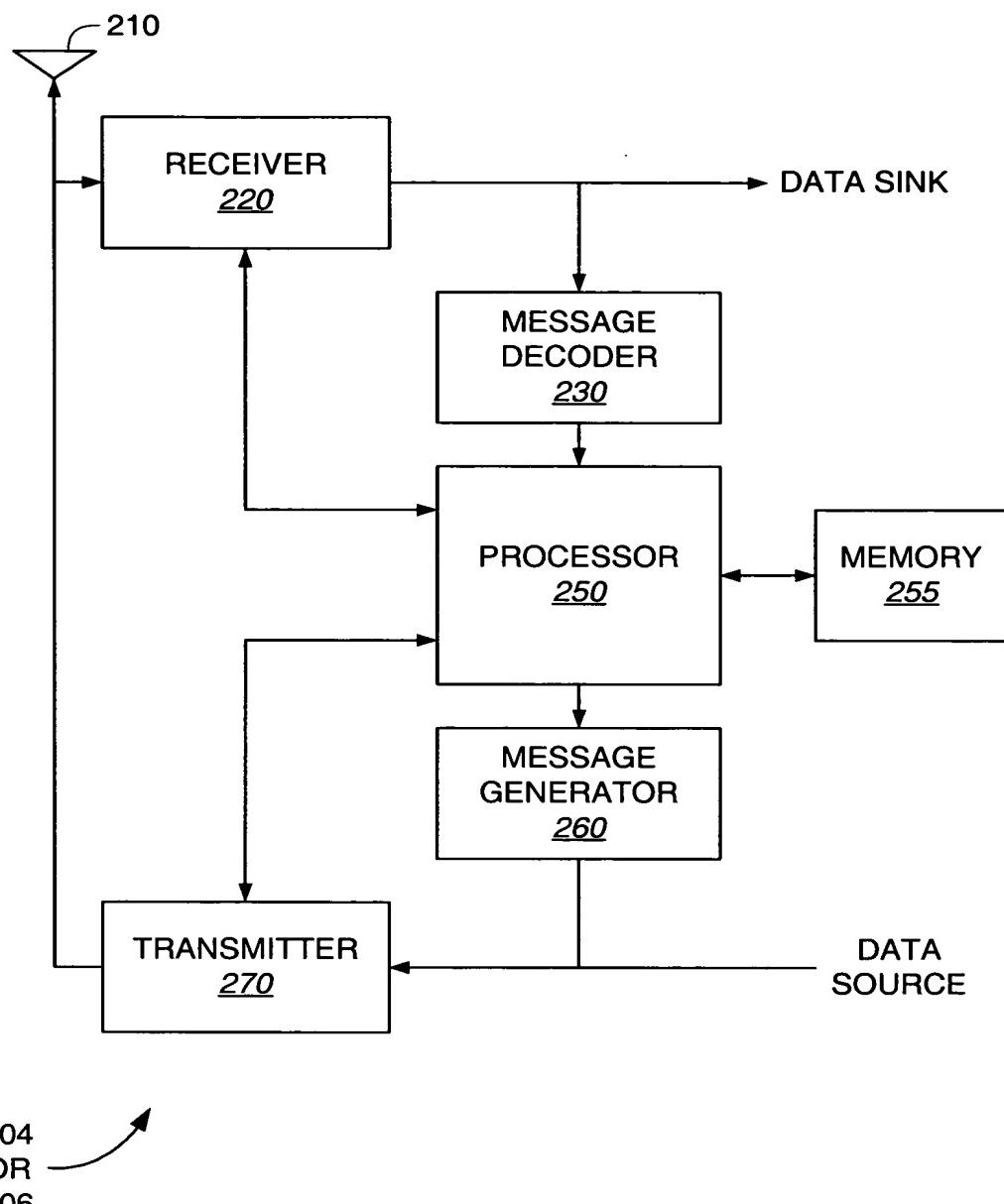
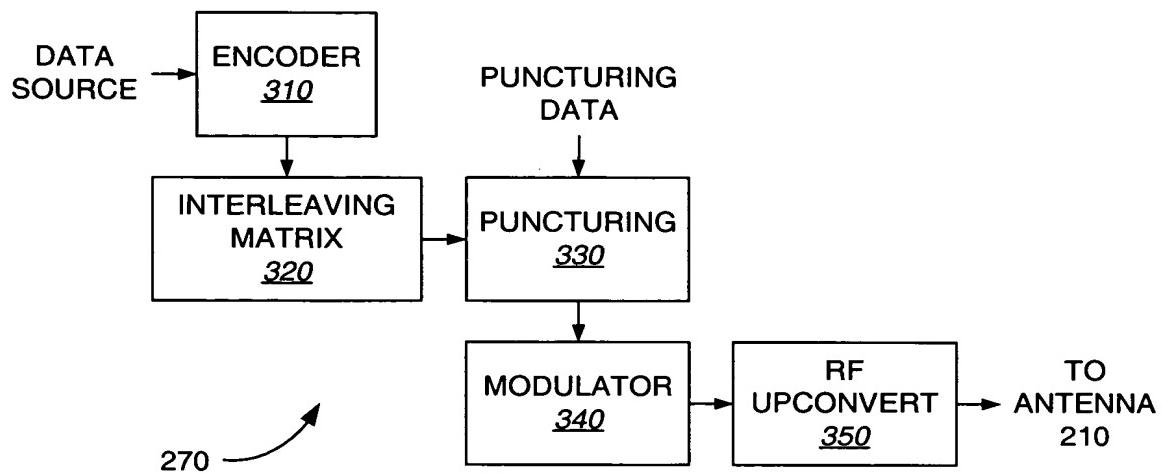
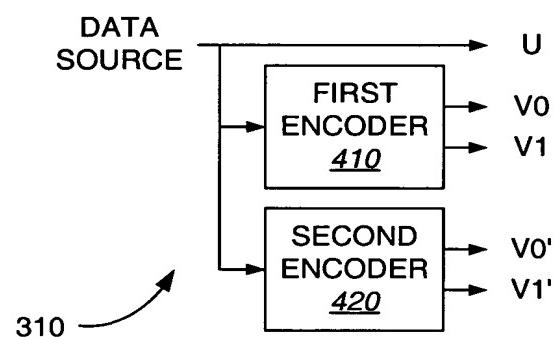
**FIG. 1**

**FIG. 2**

**FIG. 3****FIG. 4**

16QAM (5K PACKET)									
U0	...	U1279	V0 0	...	V0 1279	V0 1280	...	V0 2559	
U1280	...	U2559	V0 2560	...	V0 3839	V0 3840	...	V0 5119	
U2560	...	U3839	V0'0	...	V0'1279	V0'1280	...	V0'2559	
U3840	...	U5119	V0'2560	...	V0'3839	V0'3840	...	V0'5119	

FIG. 5

16QAM (4K PACKET)								
U0	...	U1023	V0 0	...	V0 1023	V0 1024	...	V0 2047
U1024	...	U2047	V0 2048	...	V0 3071	V0 3072	...	V0 4095
U2048	...	U3071	V0'0	...	V0'1023	V0'1024	...	V0'2047
U3072	...	U4095	V0'2048	...	V0'3071	V0'3072	...	V0'4095

FIG. 6

8PSK (3K PACKET)								
U0	...	U1023	V0 0	...	V0 1023	V0 1024	...	V0 2047
U1024	...	U2047	V0 2048	...	V0 3071	V0'0	...	V0'1023
U2048	...	U3071	V0'1024	...	V0'2047	V0'2048	...	V0'3071
U				V0/V0'				

FIG. 7

QPSK (2K PACKET)

U0	...	U1023	V0 0	...	V0 2047	V1 0	...	V1 2047
U1024	...	U2047	V0'0	...	V0'2047	V1'0	...	V1'2047
U			V0/V0'			V1/V1'		

FIG. 8

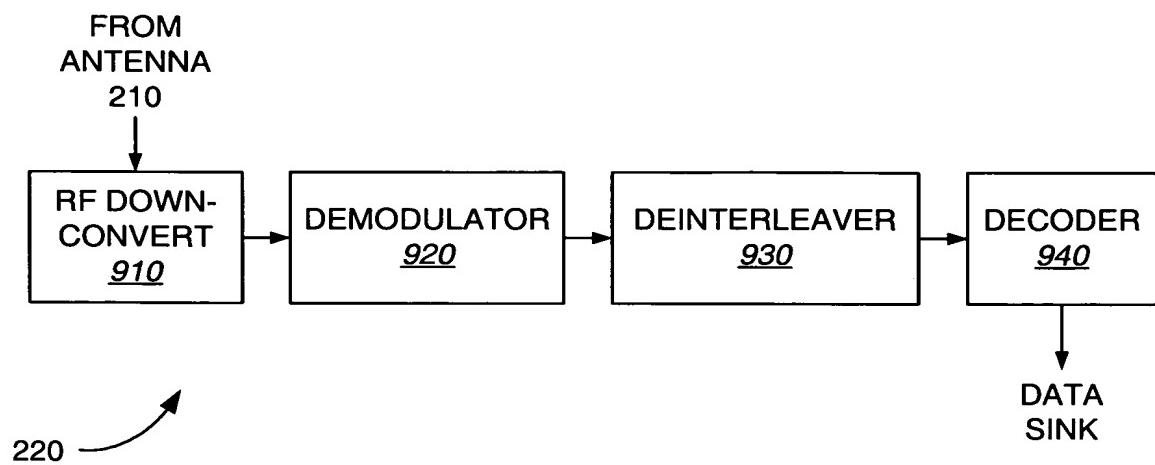
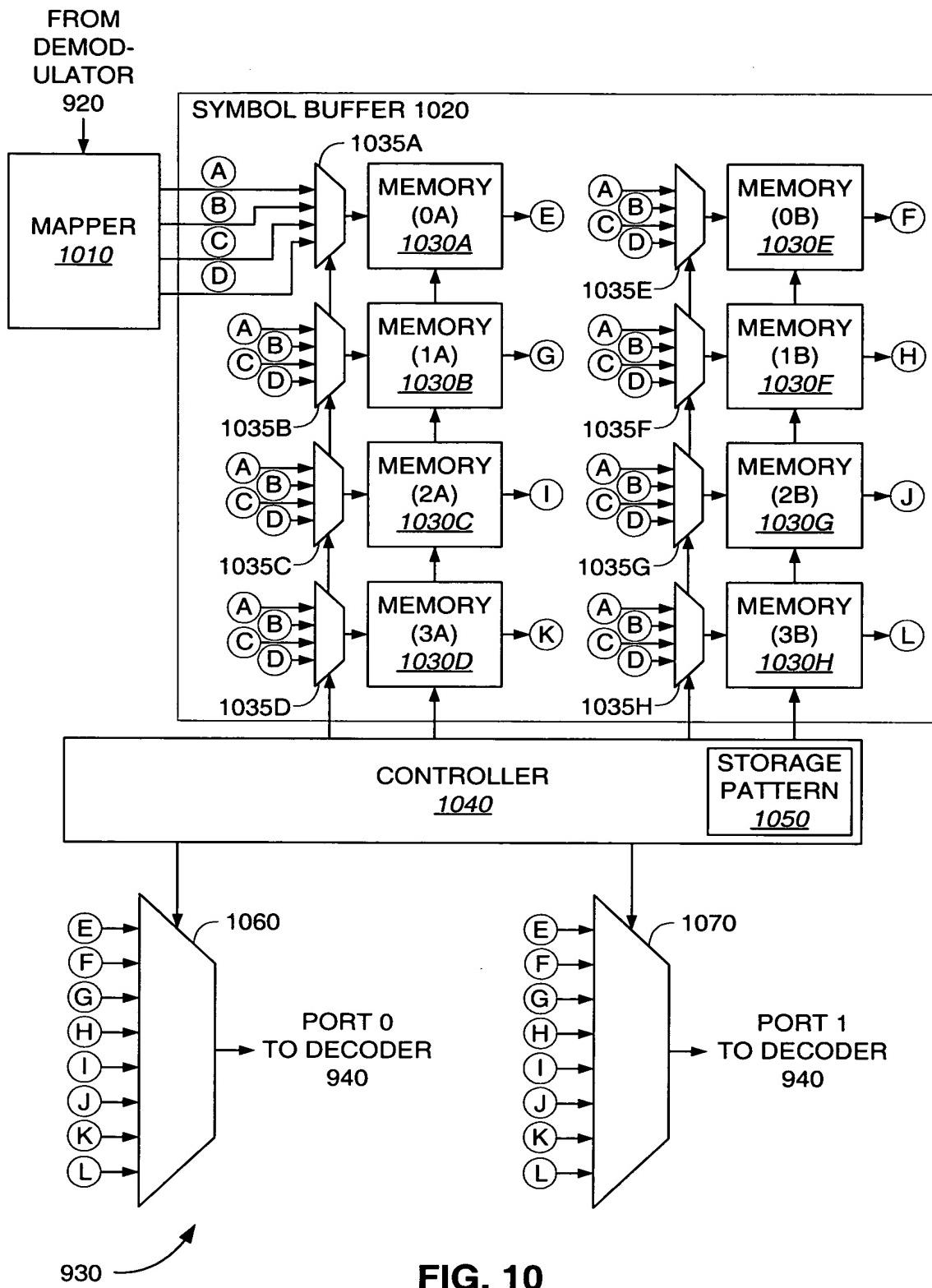
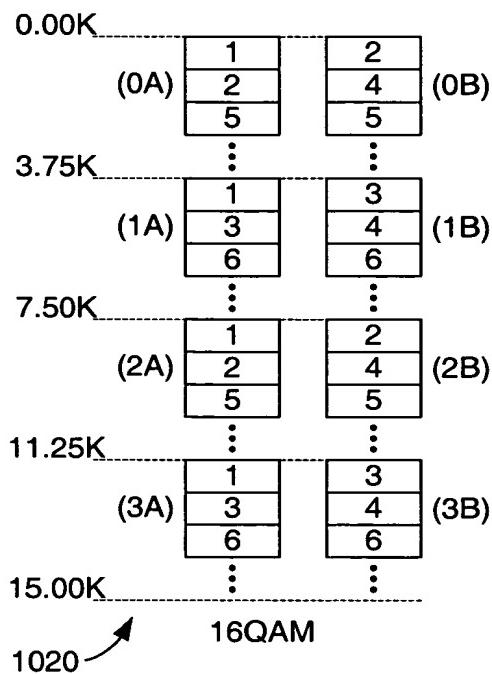
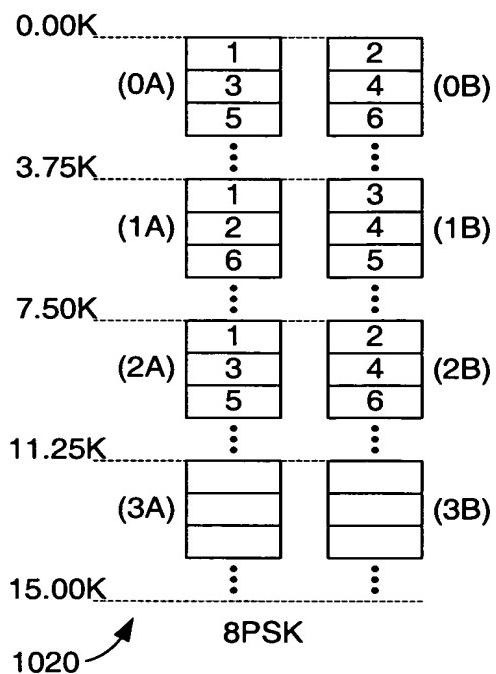
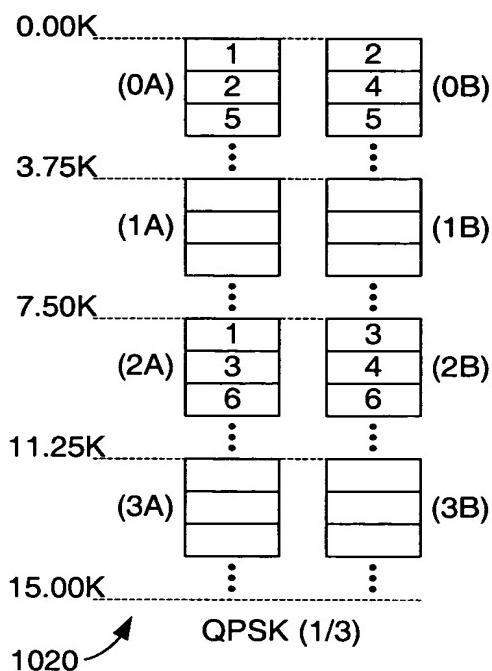
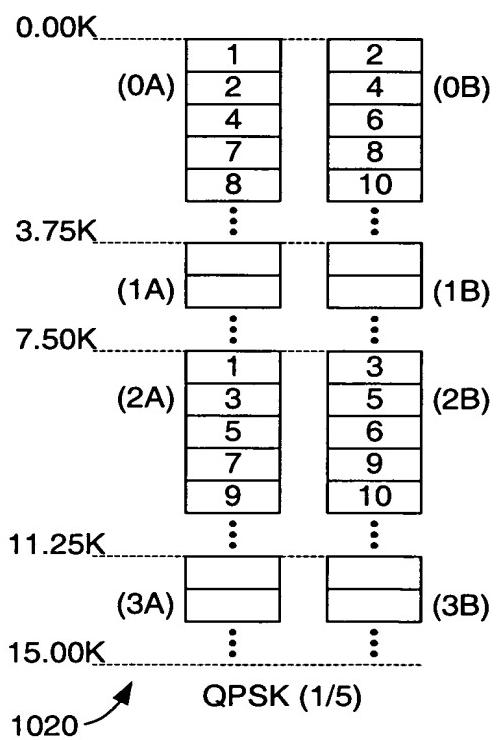


FIG. 9

**FIG. 10**

**FIG. 11****FIG. 12****FIG. 13****FIG. 14**

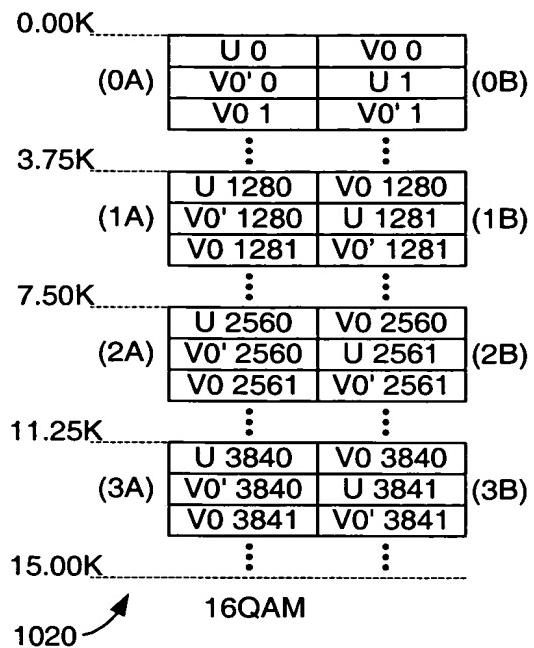


FIG. 15

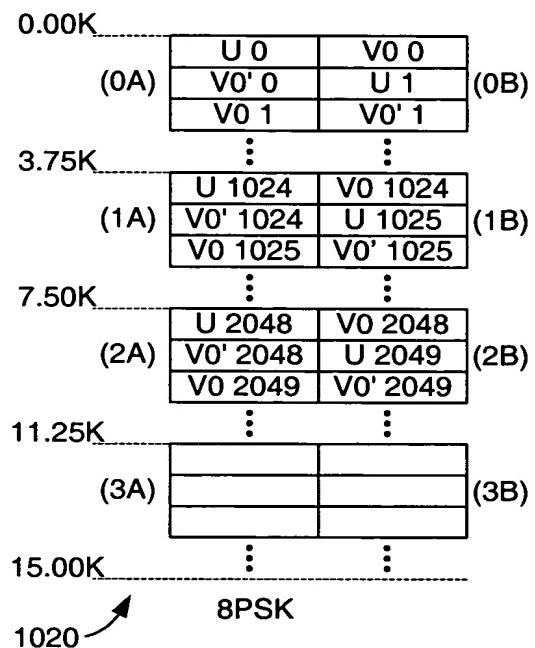


FIG. 16

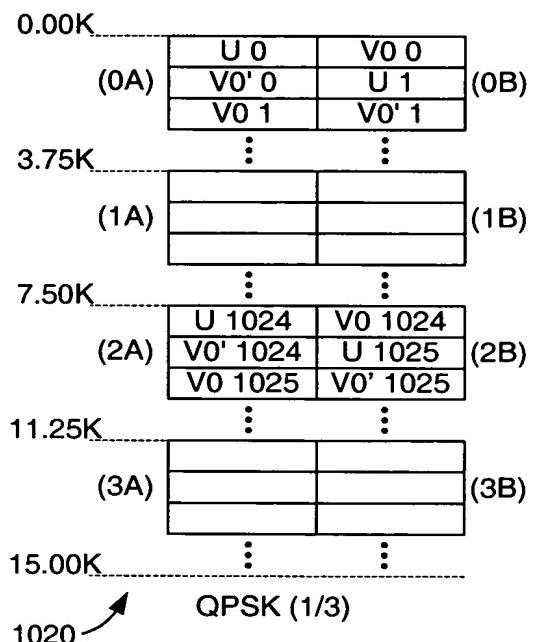


FIG. 17

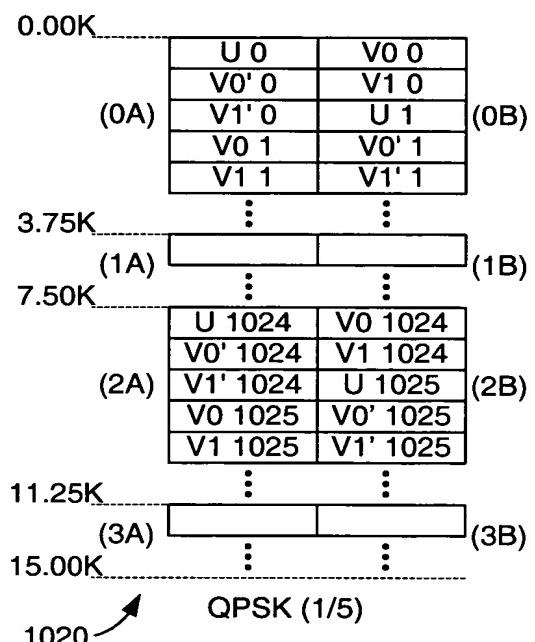
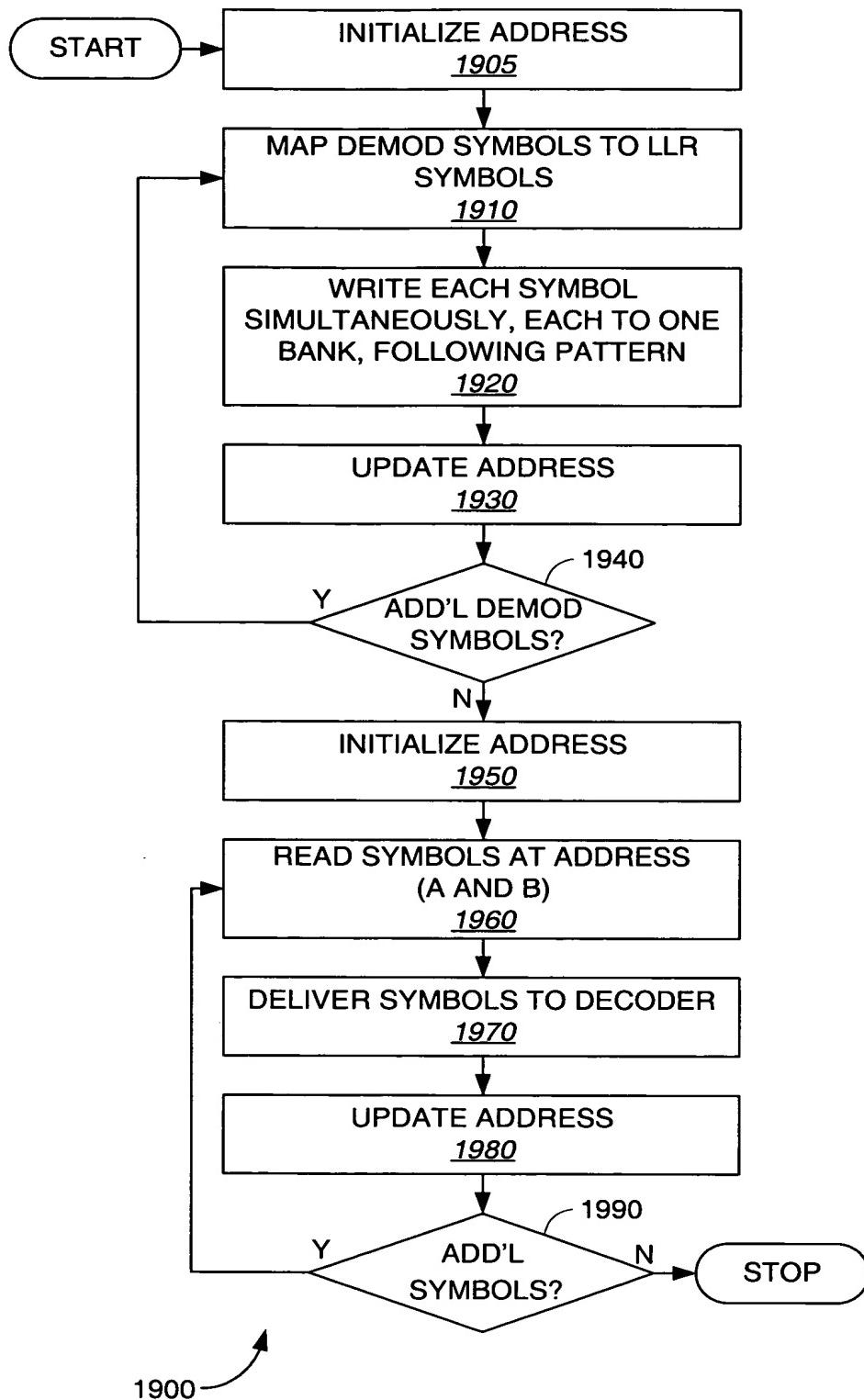
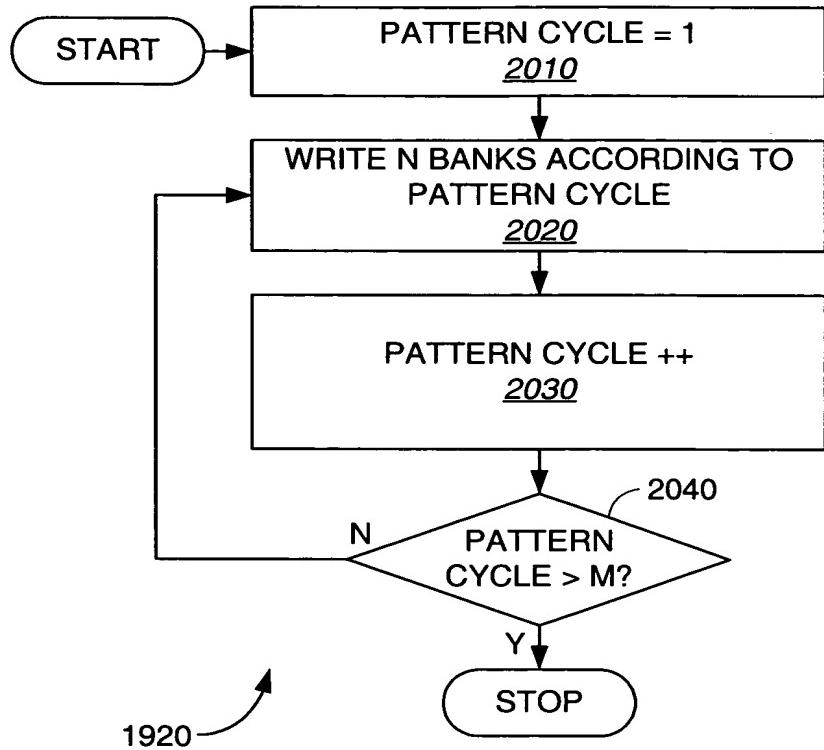


FIG. 18

**FIG. 19**

**FIG. 20**